

N73-32115-

SOLID STATE S-BAND POWER AMPLIFIER

FINAL REPORT

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for the

NASA

Marshall Space Flight Center

Huntsville, Alabama

CONTRACT NAS-8-28763

ADVANCED DEVELOPMENT



DEFENSE COMMUNICATIONS

492 River Road, Nutley, New Jersey 07110

AUGUST 1973

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**Prepared for the
NASA
George C. Marshall Space Flight Center,
Huntsville, Alabama**

Contract NAS-8-28763

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A Subsidiary of the International Telephone and Telegraph Corporation
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1. INTRODUCTION

The following is a report on the final design approach and specifications for the solid state S-band power amplifier in accord with the specifications of contract NAS-8-28763. Modifications from the design proposed in ITT Defense Communications proposal 34024 have been incorporated to improve efficiency and meet input overdrive and noise floor requirements.

2. SYSTEM DESIGN

2.1 SYSTEM BLOCK DIAGRAM

The driver and power amplifier block diagrams are shown in Drawings 1261662 and 663. A number of key features of this design should be noted. To safely apply up to one watt of power to the input 6 db of attenuation has been incorporated. Since the 12 db isolation resulting from this attenuator provides adequate input standing wave ratio, the input circulator has been eliminated. An additional stage of gain has been incorporated in the driver amplifier to compensate for the loss. There is no impact on the system noise floor, because measurements on the MSC 80064 have shown a noise figure of 7.9 db per stage, which gives a driver amplifier noise figure for two stages of 8.45 db from the formula

$$NF = F_1 + \frac{F_2 - 1}{G}$$

where NF = noise figure for the total system

F_1, F_2 = noise figure of the first and second stages

G = gain of the first stage

The total system noise floor, with an amplifier gain of 39.4 db is

$$\begin{aligned} \text{Noise Floor} &= kTB \frac{\text{dbm}}{\text{hz}} + NF + G_{\text{amp}} + \text{Bandwidth factor} \\ NF_{\text{tot}} &= -174 + 8.45 + 39.4 + 70 \\ &= \frac{-56.15 \text{ dbm}}{10 \text{ mhz}} \end{aligned}$$

To meet the out of band noise floor specification of -95 dbm/10 mhz at 2104 mhz, an additional 38.85 db of rejection must be supplied by the output filter. This is within the limits of the filter specification.

The total amplifier efficiency has been improved to 36% by newer transistors.

2.2 SYSTEM SPECIFICATIONS

The system specifications are summarized below.

<u>Title</u>	<u>NASA Spec. Requirements</u>	<u>ITT Performance</u>
Design		
Power consumption	95 watts max.	73 watts
Primary voltage	28 \pm 4 vdc	28 \pm 4 vdc
Performance		
Electrical		
Drive power	25 to 50 mw, no damage should occur by input power levels from 0.0 to 1.0 watts, cw	25 to 50 mw drive to obtain 20 watt, min. output 1 watt input drive capability
Output power	20 watts min. at optimum efficiency	23.5 watts @ efficiency
Life	Mtbf = 10,000 hrs	29,200 hour Mtbf
Terminal impedances	Input swr 1.3:1 max Output swr 1.5:1 max with all d-c removed from power amplifier	Input swr 1.25:1 Output swr 1.5:1 with or without d-c applied
Phase stability	add no more than 0.05 radian rms phase jitter to r-f input signal	0.05 radians rms phase jitter to input signal
Center frequency	2,250.5 mhz	2,250.5 mhz
Passband requirements	100 mhz, \pm 1 db centered at 2,250.5 mhz	100 mhz, \pm 1 db bandwidth center on 2,250.5 mhz

<u>Title</u>	<u>NASA Spec. Requirements</u>	<u>ITT Performance</u>
Environmental		
Temperature	meet all performance requirements when heat sink is maintained at -20 to -70 C. Thermal shock three cycles -20, +85 C.	-20 to +70 C output power and all operational characteristics
Vibration		
Random noise (5 min/plane)	20-59hz 0.04 g ² /hz 59-126 hz -9 db/octave 126-700 hz 0.40 g ² /hz 700-900 hz - 18 db/octave 900-2Khz 0.09 g ² /hz	designed to vibration specs
Sinusoidal sweep (1 octave/min.)	5-48 hz 0.125 inch 48-165 hz 15 g peak 165-2000 hz 10 g peak	designed to shock sweep specs
Shock	eight shocks per plane, 3 planes, 50 g for 11 msec.	designed to shock specs
Acceleration	1 min of 100 g in each of three planes	designed to acceleration specs
Vacuum and pressurization	within 24 hr. period leak rate shall be less than 1.0 psi when pressurized to 15 psig in a vacuum of 1.5 x 10 ⁻⁶ mmhz	designed to vacuum and pres- surization specs
Humidity	MIL-STD-810, Method 507.1 Procedure 1	Designed to Humidity specs
R-F interference	per MIL-I-6181	per MIL-I-6181
Acoustical noise	per MIL-STD-810	per MIL-STD-810
Output load isolator	20 db isolation over passband	20 db, minimum 25 db, typical over passband

<u>Title</u>	<u>NASA Spec. Requirements</u>	<u>ITT Performance</u>
In-band spurious responses	60 db below output carrier measured within a 4 mhz band- width at output frequency 2,250.5 mhz	60 db below rated output at 2250.5 mhz
Output noise level	Internal noise gen. 2104 \pm 5 mhz, less than -95 dbm	-95 dbm, minimum -100 dbm, typical
Output filter	Bandpass filter to reduce output noise spectrum of power amplifier	Output noise floor -95 dbm/10 mhz, Harmonics -60 db

2.3 SYSTEM MECHANICAL LAYOUT

The system mechanical layout is shown in the sketch, Drawing 1261673-B. The input circulator has been replaced by a fixed attenuator and an additional preamplifier stage has been incorporated.

Sketches and drawings used in the construction of this amplifier are bound together at the end of this document.

3. DRIVER AMPLIFIER SECTION

The driver section consists of the following components:

- input attenuator
- 3 stage amplifier
- matching networks

The specified input standing wave ratio of 1.3:1 has been satisfied as summarized in the following table.

Preamplifier input standing wave ratio	2.0:1 max.
Input standing wave ratio of fixed attenuator, variable attenuator and preamplifier.	1.28:1 max.
Specification requirements	1.3:1 max.

For 1 watt or +30 dbm applied to the input, the preamplifier receives 126 mw or +21 dbm, which is within acceptable limits.

3.1 INPUT ATTENUATOR

The fixed input attenuator satisfies the following specifications:

Specification	
Attenuation	dB
Input standing wave ratio	1.15:1
Power rating	1 watt at 75 C
Impedance	50
Frequency range	2.250 ghz \pm 50 mhz
Size	5/16" diam., 1.5" long
Connectors	SMA

3.2 LOW LEVEL DRIVER AMPLIFIER STAGES

A circuit diagram of the three stage amplifier is shown in Drawing 1261662.

The nominal output power is 3.5 watts with 3 mw input giving a gain of 30.7 db.

The overall specifications required for this amplifier chain are:

Frequency range	2.2-2.3 ghz
Gain	30 db
Gain flatness	1.5 db over band
Max. power output	5 watts (saturated)
Input standing wave ratio	2:1
Impedance	50 (nominal)
Maximum permissible	
Load mismatch	1.5:1 standing wave ratio
Maximum power in.	150 mw.
Efficiency	33%
Power requirements:	23-28 vdc and (last stage) 22 vdc zener regulated

The driver amplifier was optimized and tested for -20 to +72 C. Minimum power output was 3.6 watts sufficient to drive the power amplifier. The power output variation was less than ± 0.5 db and the power consumption less than 14 watts.

The driver amplifier was driven with 1 watt of CW from 2.2 to 2.3 ghz with no apparent deterioration in performance. The second amplification stage is designed to saturate and cut off drive to the power stages to prevent catastrophic failure. From 25 watts to 50 watts drive the amplifier performed satisfactorily.

The input standing wave ratio with d-c power applied was less than 1.25:1. With d-c disconnected it was less than 1.5:1 over the band.

The amplifier matching networks and chassis are shown in Drawings 1261666, 1261667, and 1261661; the schematic is shown in Drawing 1261662.

4. POWER AMPLIFIER SECTION

The power amplifier consists of

- hybrid dividers
- hybrid combiners
- power stages
- output isolator
- output filter

The design and specification of these modules are discussed in the following sections.

4.1 HYBRID DIVIDERS

The 2:1 divider modules composing the 4:1 hybrid meet the following specifications:

Coupling	3 db \pm .1 db
Frequency band	2.25 ghz \pm 50 mhz
Insertion loss	.07 db
Power rating	30 watts @ 20 C
Impedance	50
Standing wave ratio	1.25:1 max.
Isolation	20 db min.

The dividers are of stripline construction using 1/8" thick glass impregnated teflon. The top and bottom substrate drawings for these hybrids are shown in drawings 12611671 and 1261672.

4.2 HYBRID COMBINER

The hybrid 4:1 combiner consists of three 2:1 combiners meeting specifications as shown in section 4.1. The substrate views are drawings 1261669 and 1261670.

4.3 POWER STAGES

The individual power stages consist of an MSC 4005 transistor. They are hybrid combined as shown in the following figure giving 29 watts output and 9.2 db gain.

The total of 4 stages hybrid combined, meet the following specifications:

Frequency band	2.2 - 2.3 GHz
Gain	db
Gain flatness	0.5 db
Max. power out.	watts
Input standing wave ratio	1.5:1 max.
Efficiency	%
Max load mismatch	1.5:1
Power requirements	23-28 vdc

Temperature data on the Solid State S Band Amplifier is shown in Table 4-I.

The power amplifier schematic and matching networks are shown in Drawings 1261663 and 1261665.

4.4 OUTPUT ISOLATOR

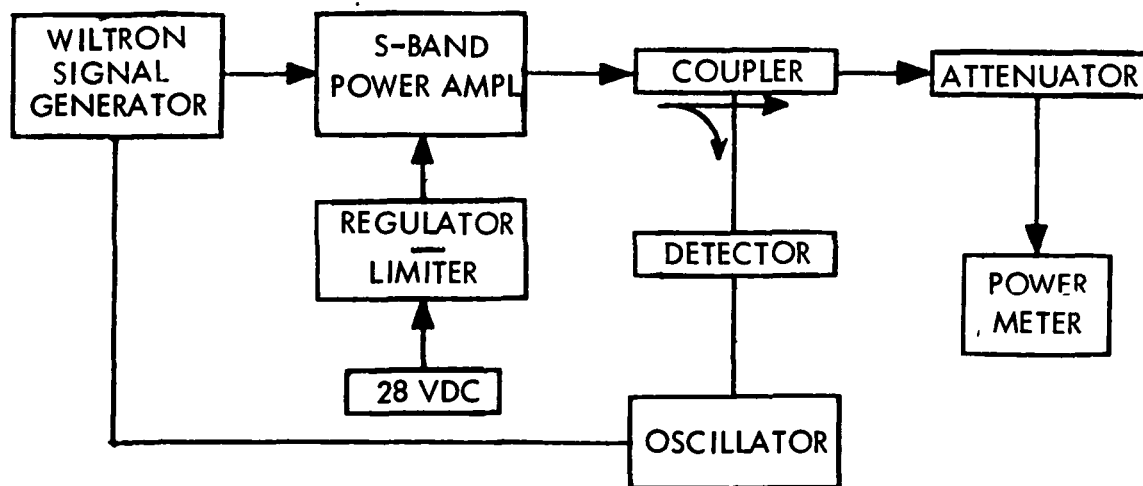
The output isolator was tested to and meets the following specifications:

Specifications

Nominal frequency	2.2 - 2.3 ghz
Bandwidth for 20 db isolation.	100 mhz
Input & output impedance	50 ohms
Standing wave ratio (max.)	1.2
Insertion loss in passband	.25 db max. (at 25 watts)
Load capability	30 watts cw @ 75 C
The above specifications must be met over temp range.	-20 to +70 C
Addington part number	101101203
Probe coupling	-40 db \pm 2 db

TABLE 4-I
TEMPERATURE TEST OF SOLID STATE
S-BAND AMPLIFIER

<u>Temp, C</u>	<u>R-F P_{out}, W</u>	<u>I_c, A</u>	<u>Voltage In</u>	<u>Voltage Out</u>	<u>Total Power In, W</u>	<u>Efficiency, %</u>
20	22.8	2.8	28.00	26.83	78.5	29.0
10	22.8	2.8	"	"	78.5	29.0
5	23.6	2.94	"	"	82.2	28.8
0	24.0	2.96	"	"	86.0	28.6
5	24.0	3.00	"	"	84.0	28.6
10	23.7	2.95	"	"	82.55	28.8
15	23.7	2.95	"	"	82.5	28.8
20	23.5	2.95	"	"	82.5	28.5
25	23.1	2.90	"	"	81.5	28.5
<hr/>						
30	22.7	2.85	"	"	80.0	28.3
35	22.5	2.85	"	"	80.0	28.2
40	22.1	2.85	"	"	80.0	27.7
45	22.0	2.84	"	"	79.5	27.7
50	21.5	2.81	"	"	79.0	27.5
55	21.4	2.78	"	"	78.0	27.3
60	21.0	2.77	"	"	77.5	27.2
65	20.8	2.74	"	"	76.8	27.2
70	20.3	2.72	"	"	76.0	26.8
75	20.2	2.72	"	"	76.0	26.7



Test Set-Up, Block Diagram

4.5 OUTPUT BANDPASS FILTER

The specifications given below ensure that the system will have the required -95 dbm/10 mhz noise floor at 2104 mhz with 5 db margin to allow for excess drive conditions.

Center freq	2.25 ghz
Passband	2.2 to 2.3 mhz
Ripple	0.2 db
Attenuation	45 db at 2.104 ghz \pm 5 mhz 40 db from 4.0 to 10 ghz
Insertion loss	0.4 db msx. , target 0.25
Input standing wave ratio	1.25:1
Power capability	30 watts max.
Phase deviation from linear	\pm 6 degrees over the 100 mhz band
Temperature	-20 to +70 C
Reliability	10^8 hrs.

The filter was tested to and meets these specifications. The outline drawing for the filter is shown in 1261674.

5. VOLTAGE AND CURRENT LIMITER

The voltage and current limiter is shown in figure 5-1. It provides voltage limiting to a maximum of 27.3 vdc by a saturated series transistor. Current limiting is provided by a monostable multivibrator triggered by an excess current flow and acting as crowbar to reduce the voltage to zero and protect the series pass transistor. The regulator will recover automatically upon removal of the overload.

The voltage limiter--current limiter was tested and performed satisfactory. The total drop across the regulator was measured 1.15 volts. A 6 ampere current tripped the current limiter. It reset automatically when the current was reduced to normal. The voltage limiter was set to an upper limit of 28 volts and remained exactly 28 volts for inputs up to 40 volts.

6. TEST PROGRAM

The following document lists all test procedures which adequately verify that the breadboard will meet specification requirements. These test procedures are submitted to MSFC for approval in compliance with Exhibit A Scope of Work, Contract No. NAS8-28763.

6.1 LIST OF TESTS REQUIRED AND SPECIFICATION LIMITS

<u>Test Description</u>	<u>Specification</u>
6.1.1 Input standing wave ratio	1.3:1 (17.8 db return loss)
Output standing wave ratio (without drive)	1.5:1 (14 db return loss)
6.1.2 Power output (2.25 ghz)	20 watts min.
6.1.3 D-C power input	95 watts max.
6.1.4 Input overdrive (R-F)	1 watt max.
Normal drive	25-50 mwatts
6.1.5 Bandwidth (± 1 db)	100 mhz
6.1.6 Output noise	-95 dbm of 2104 mhz, 10 mhz band
6.1.7 Spurious outputs	60 db below carrier within 4 mhz band centered at 2.250.5 mhz
6.1.8 Temperature (Meeting performance requirements)	-20 to +70 C

6.2 DESCRIPTION OF TESTS

The test equipment shown in the block diagram are suggested and alternate equipment can be used.

6.2.1 Input, Output Standing Wave Ratio

To measure input and output impedances, a Wiltron 610-C Signal Generator and Narda Directional Coupler were set up as shown in Figure 6-1. Measurements are fast

and accurate; the Narda High Directivity Coupler has a directivity of greater than 40 db from 10 to 3500 mhz and the accuracy of the readout (Return Loss) is ± 0.3 db.

6.2.2 Power Output

The power output is measured using a Thermoelectric calorimeter power meter. The power into the calorimeter is reduced using calibrated attenuators as shown in Figure 6.2.

For the overdrive test, a travelling wave tube Servo 2220 is used to amplify the output of the signal generator and provide 1 watt to the input of the S-Band power amplifier. The normal input is 25-50 mwatts at 2.25 ghz.

6.2.3 D-C Power Input

The input power is measured accurately using a digital voltmeter and a millimeter. Since the power input can be calculated at 28 ± 4 vdc, the efficiency can also be calculated using the set up shown in Figure 6.3.

6.2.4 Input Overdrive

According to specification, the S-Band amplifier should survive 1 watt overdrive. This drive is derived using a travelling wave tube amplifier following the signal generator as shown in Figure 6.2. During overdrive all parameters are monitored.

6.2.5 Bandwidth

The instantaneous bandwidth is measured using a Wiltron sweep generator as shown in Figure 6.3 set-up. The detected output is displayed on the calibrated screen of the oscilloscope for the nominal input power of 25 watts. A frequency counter is provided to calibrate the marker of the sweep generator. Photos can be taken of the 100 mhz bandwidth or greater to show the bandpass characteristic of the amplifier filter.

6.2.6 Output Noise

Measurement of noise at 2104 mhz is accomplished using a spectrum analyzer and applying the necessary correction factors. This measurement has to be performed under signal conditions in order to excite the class-C power amplifiers. Only 10 db of attenuation is used at the output of the amplifier in order to maintain a margin since the noise level of the spectrum analyzer at 2.1 ghz is -117 dbm. To prevent overloading of the spectrum analyzer, a bandpass filter is used at 2104 mhz reflecting the 2.25 ghz output signal by at least 40 db. The set-up is shown in Figure 6.4. The yig filter is set manually to 2104 mhz.

6.2.7 Spurious Outputs

Spurious outputs within a bandwidth of 4 mhz centered at 2,250.5 mhz is measured using the spectrum analyzer and necessary attenuator to reduce the level and prevent overloading of the spectrum analyzer. The set-up is shown in Figure 6.5.

6.2.8 Temperature

The set-up of Figure 6.6 is used for the temperature test. All monitoring cables are brought out and continues monitoring of chassis temperature and power output is performed during the temperature variation. As shown all performance requirement can be checked during the test.

6.3 TEST DATA SHEET

(a)	Input standing wave ratio	<u>1.28</u>	(<u>18</u> db return loss)
(b)	Output standing wave ratio	<u>1.12</u>	(<u>25</u> db return loss)
(c)	Pout (2.25 ghz)	<u>23.0</u>	watts
(d)	Pin - Vin	<u>28.14</u>	volts
	Iin	<u>2.9</u>	amperes
	Pin	<u>81.0</u>	watts
(e)	Output Noise	* <u>> 87</u>	dbm/10 mhz
(f)	Spurious Output	<u>No Spurs > 70</u>	db at 2250.5 mhz

* Limited by Spectrum Analyzer

ITTDCD Engineering

ITTDCD Quality Assurance

Michael D. Dymally
E. Barony

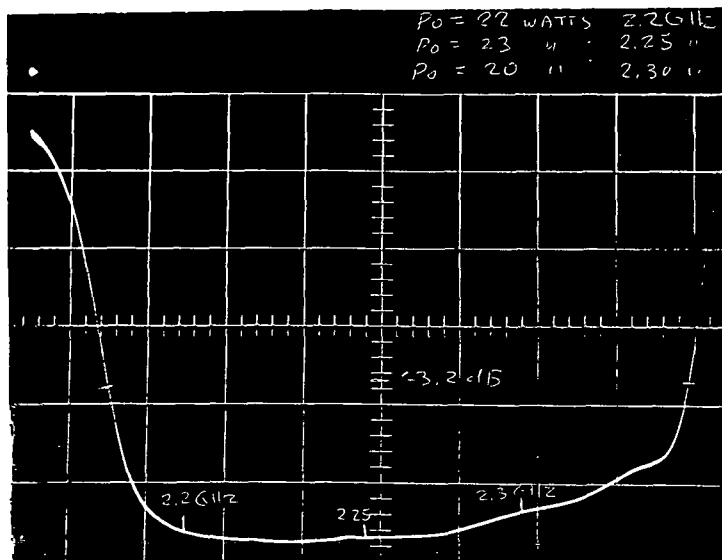
Date

8/15/73

Date

8/15/73

(g) Bandwidth



Photograph
(Instantaneous Bandwidth)

ITTDCD Engineering

Michael D. Dymally

ITTDCD Quality Assurance

E. Baranyi



Date

8/15/72

Date

8/15/73

6.4 LIST OF TEST EQUIPMENT REQUIRED

1. Attenuator, 20 db, 10 db, 50 watt; Narda Model 765-20, 765-10.
2. Attenuator, 20 db, 2 watt, Weinschell Model 50-20.
3. Attenuator, Variable 0 to 20 db, Narda Model 792 FM.
4. Clip-on millimeter 0 to 10 amps, Hewlett Packard Model 428B.
5. Crystal Detector, Hewlett Packard Model 423A.
6. Digital Voltmeter, Hewlett Packard Model 3440A.
7. DC Multifunction unit, Hewlett Packard Model 3444A.
8. Frequency counter, Systron Donner Model 6316A.
9. Thermoelectric Calorimeter Model N-685-2 PRD.
10. Power Meter Model 6685 PRD.
11. Signal Generator, S-Band, Hewlett Packard Model 8616A.
12. Sweep Generator, Frame Model 610C, Head 6112C Wiltron.
13. Tektronix Model 531A.
- ~~14. VSWR Autotester, Mode 63N50 Wiltron.~~
- ~~15. Logarithmic Level Meter Model 501 Wiltron.~~
16. Power Supply, Lambda Model LH125 FM.
17. Spectrum Analyzer
 - RF Section Model 8555A Hewlett Packard
 - IF Section Model 8552A Hewlett Packard
 - Display Section Model 141 S Hewlett Packard
 - Automatic Preselector Model 8445 Hewlett Packard
18. Directional Coupler, Model 3043-20 Narda.
19. Traveling Wave Tube Amplifier, Model 2220 Servo Microwave Amplifier.

Note 1: Item 14 was deleted prior to test

Item 15 was changed to NARDA 3093 High Directivity Coupler.

Note 2: All equipment used for this test was under current calibration.

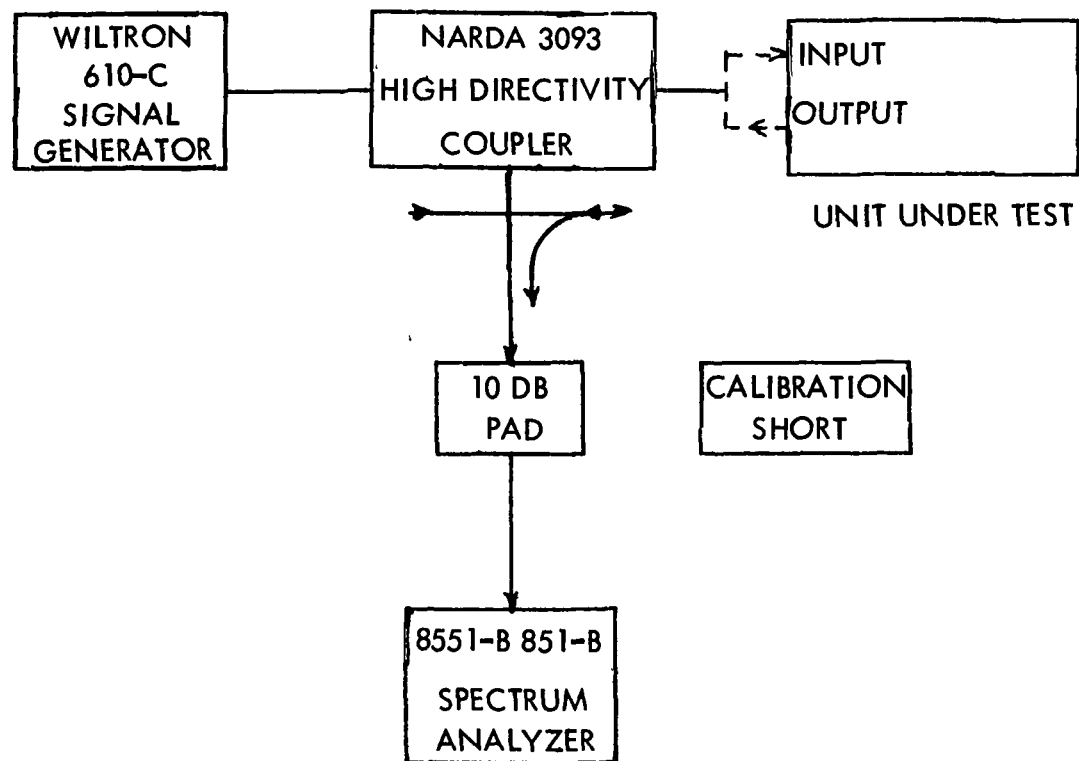


Figure 6.1 Input & Output Standing Wave Ratio

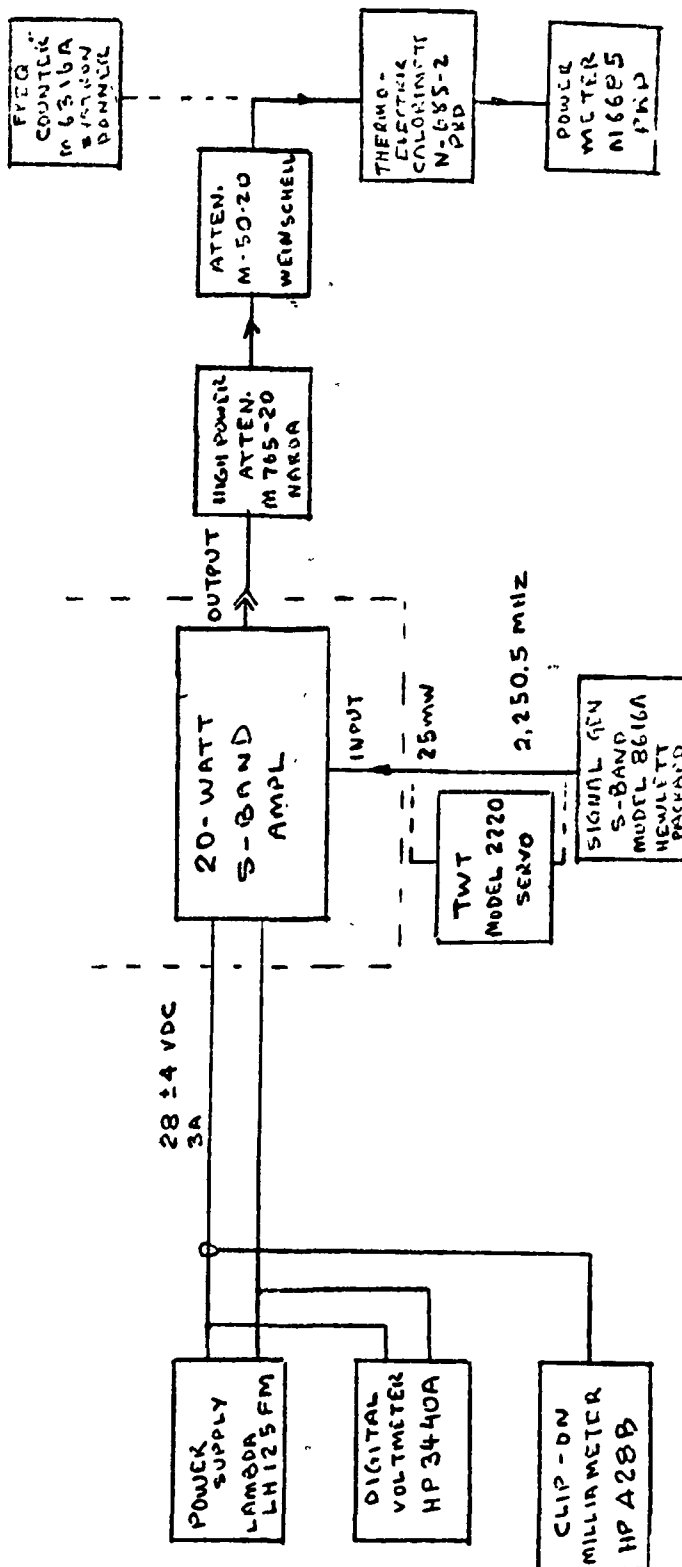
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	A	28528	20
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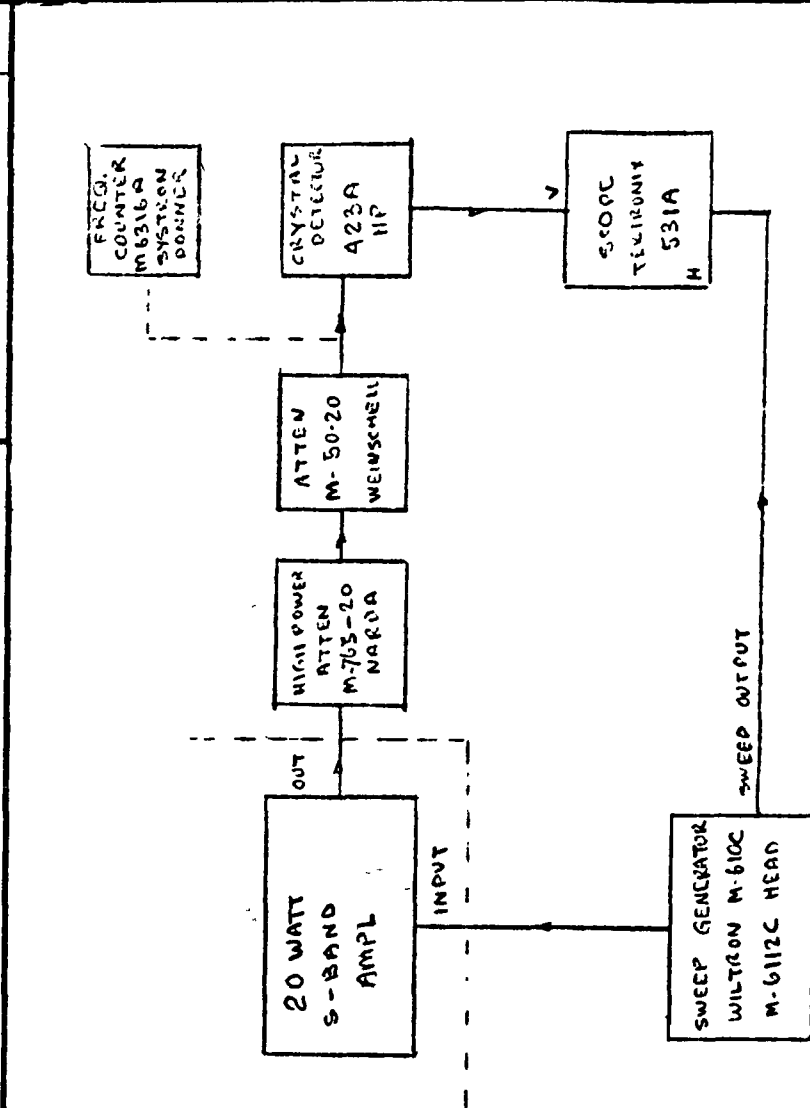
POWER OUTPUT AND EFFICIENCY MEASUREMENTS

PLUS INPUT OVERDRIVE

FIG 6.2

6.2

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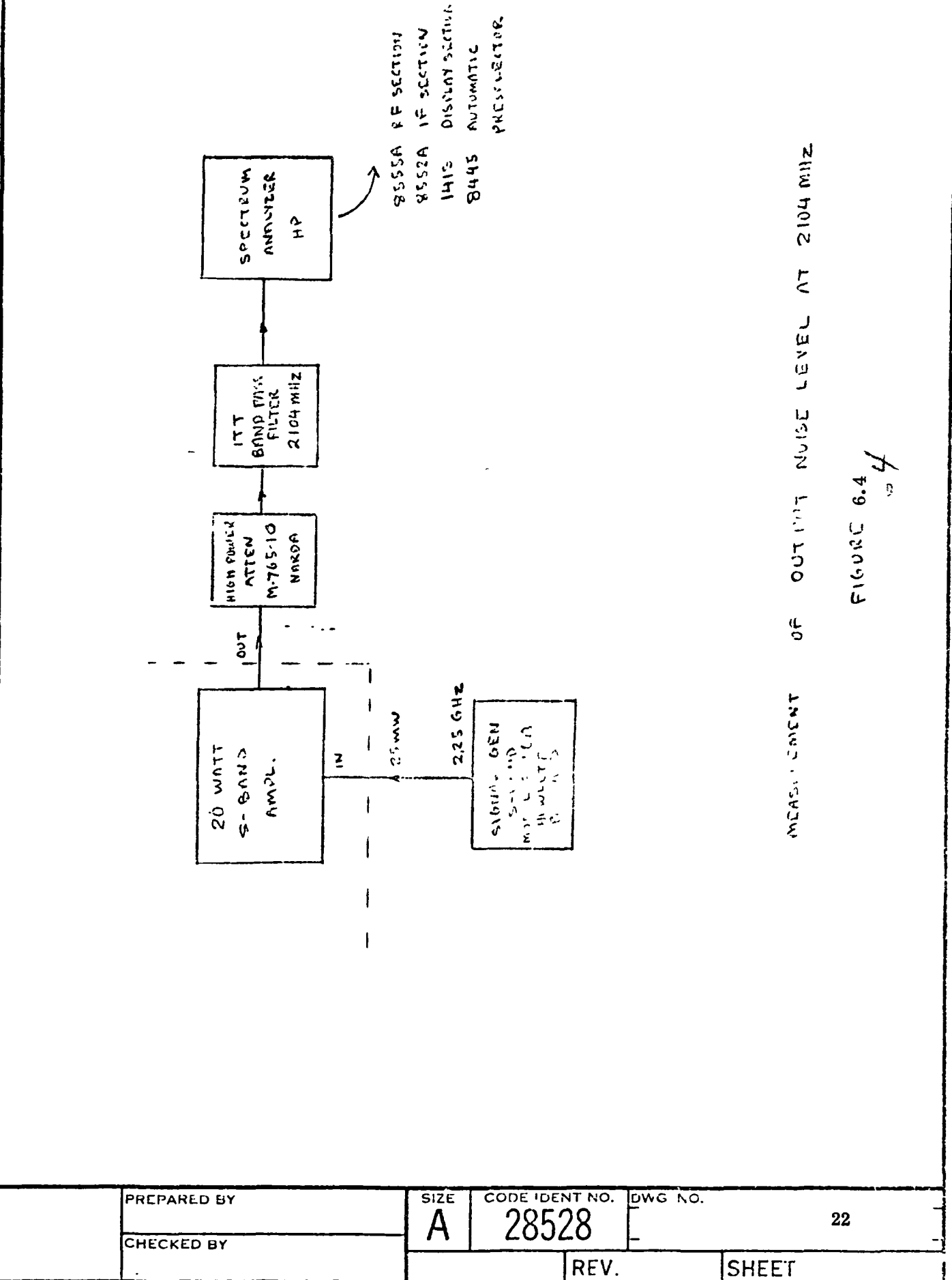


BANDWIDTH MEASUREMENT, SWEEP

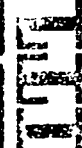
6.3
FIGURE 6.3

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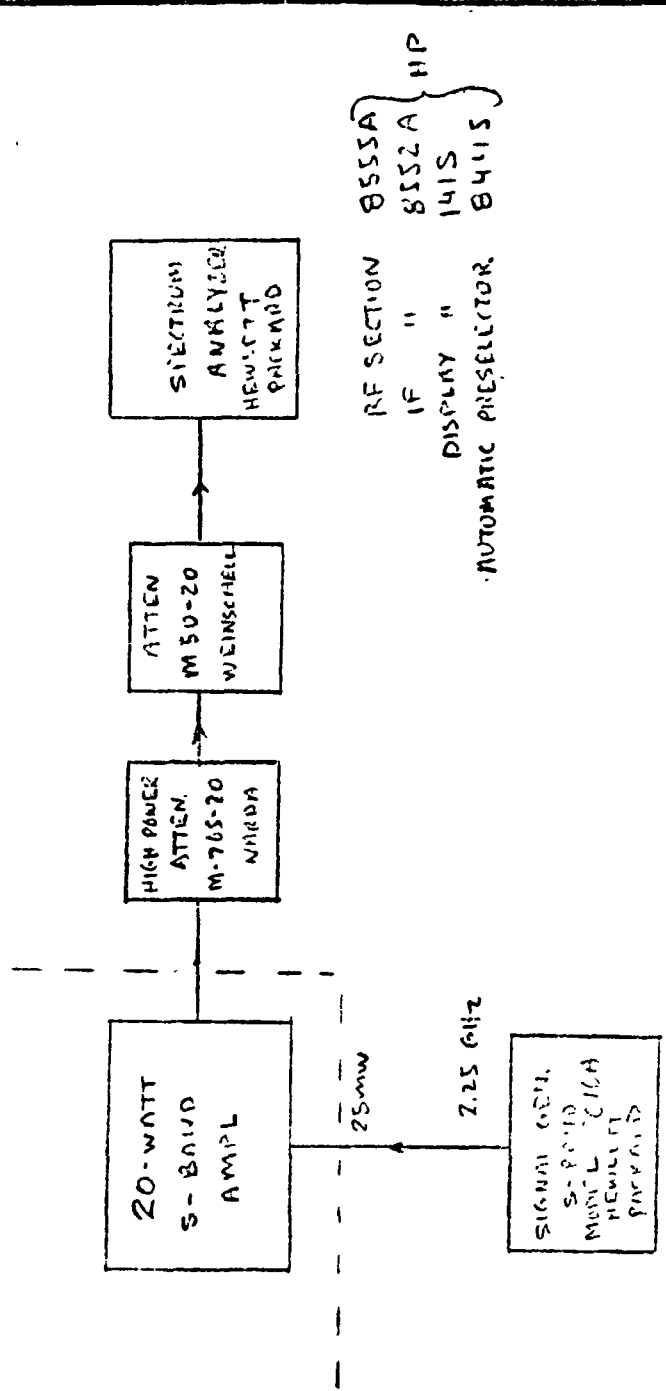
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RF SECTION 8553A
IF " 8552A HP
DISPLAY " 141S
AUTOMATIC PRESELECTOR 8411S

MEASUREMENT OF SPURIOUS OUTPUTS

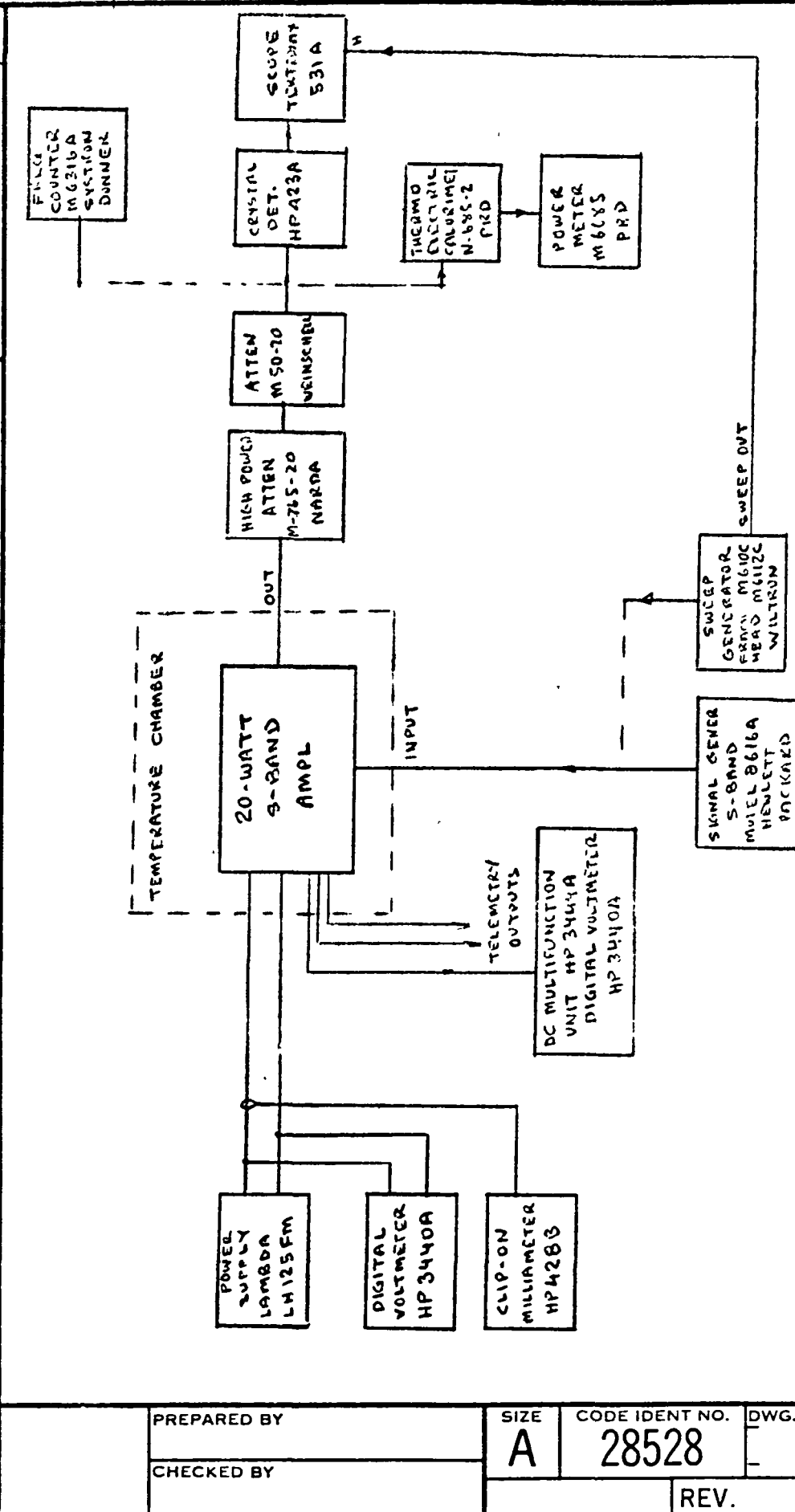
FIGURE 6.5

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DRAWING NUMBER



TEMPERATURE TEST, TELEMETRY OUTPUTS PLUS TURN-OFF CAPABILITY.

FIGURE 6.6
6.6

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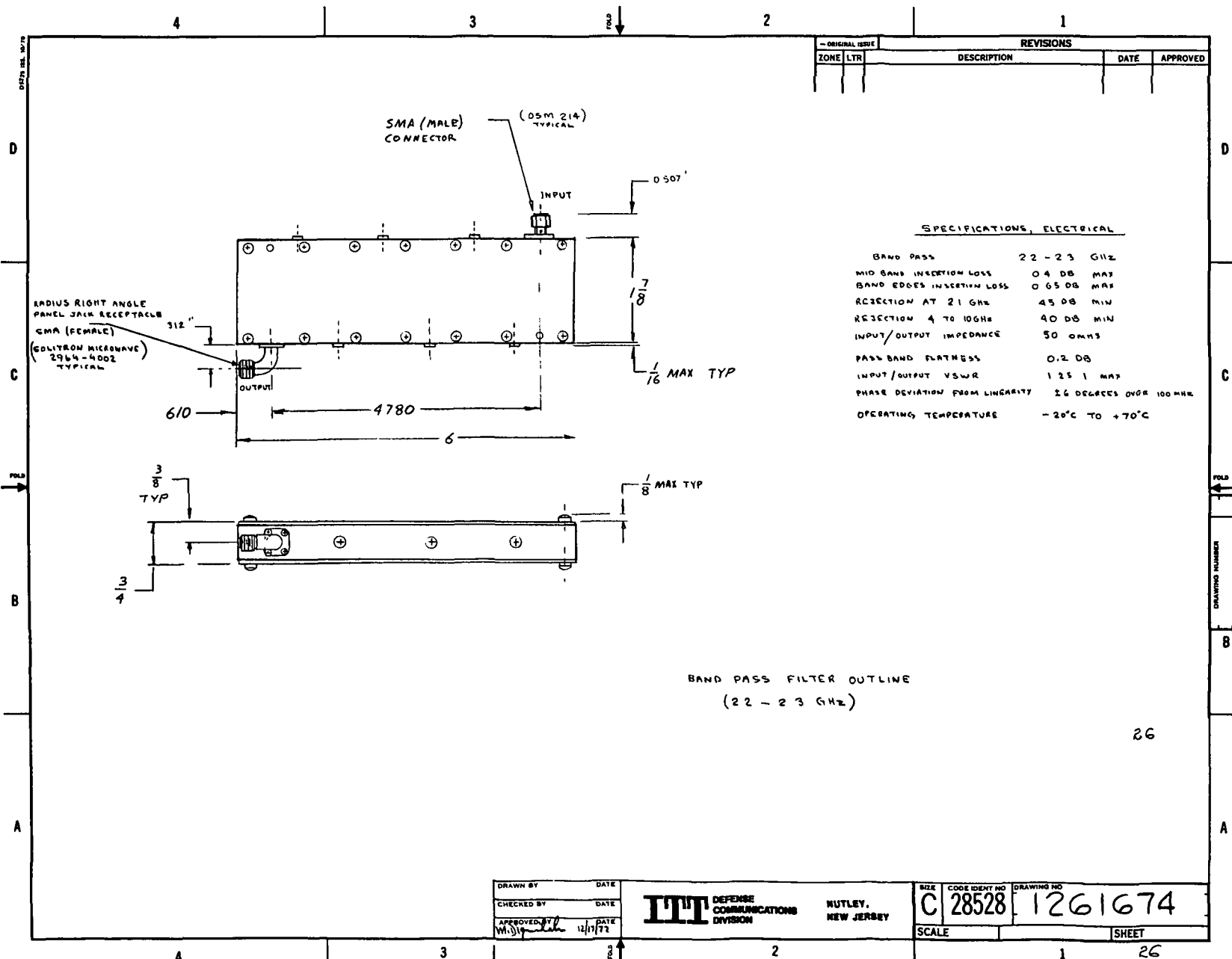
SHEET

LIST OF DRAWINGS

The following drawings are bound in order and referred to by drawing number:

1261674 Bandpass Filter Outline
~~1261678 Mechanical Layout~~
1261672 90 Degree Input Hybrids (Top)
1261671 90 Degree Input Hybrids (Bottom)
1261670 90 Degree Output Hybrids (Top)
1261669 90 Degree Output Hybrids (Bottom)
1261664 Telemetry & Functions, Circuit Diagram
1261668 0.8 Watt Amplifier Matching Networks
1261667 3.5 Watt Amplifier Matching Networks
1261666 Class A Amplifier Matching Networks
1261665 8 Watt Amplifier Matching Networks
1261663 Solid State Power Amplifier, Block Schematic*
1261662 Solid State Driver Amplifier, Block Schematic*
1261661 Driver Amplifier Chassis
1261659 Power Amplifier Chassis
1261673-B Engineering Sketch of Main Frame

*Sheets 2, 3, and 4 of both Drawings are parts lists; because of size differences, these are bound following the package of C Size Drawings.



- ORIGINAL ISSUE		REVISIONS		
ZONE	LTR	DESCRIPTION	DATE	APPROVED

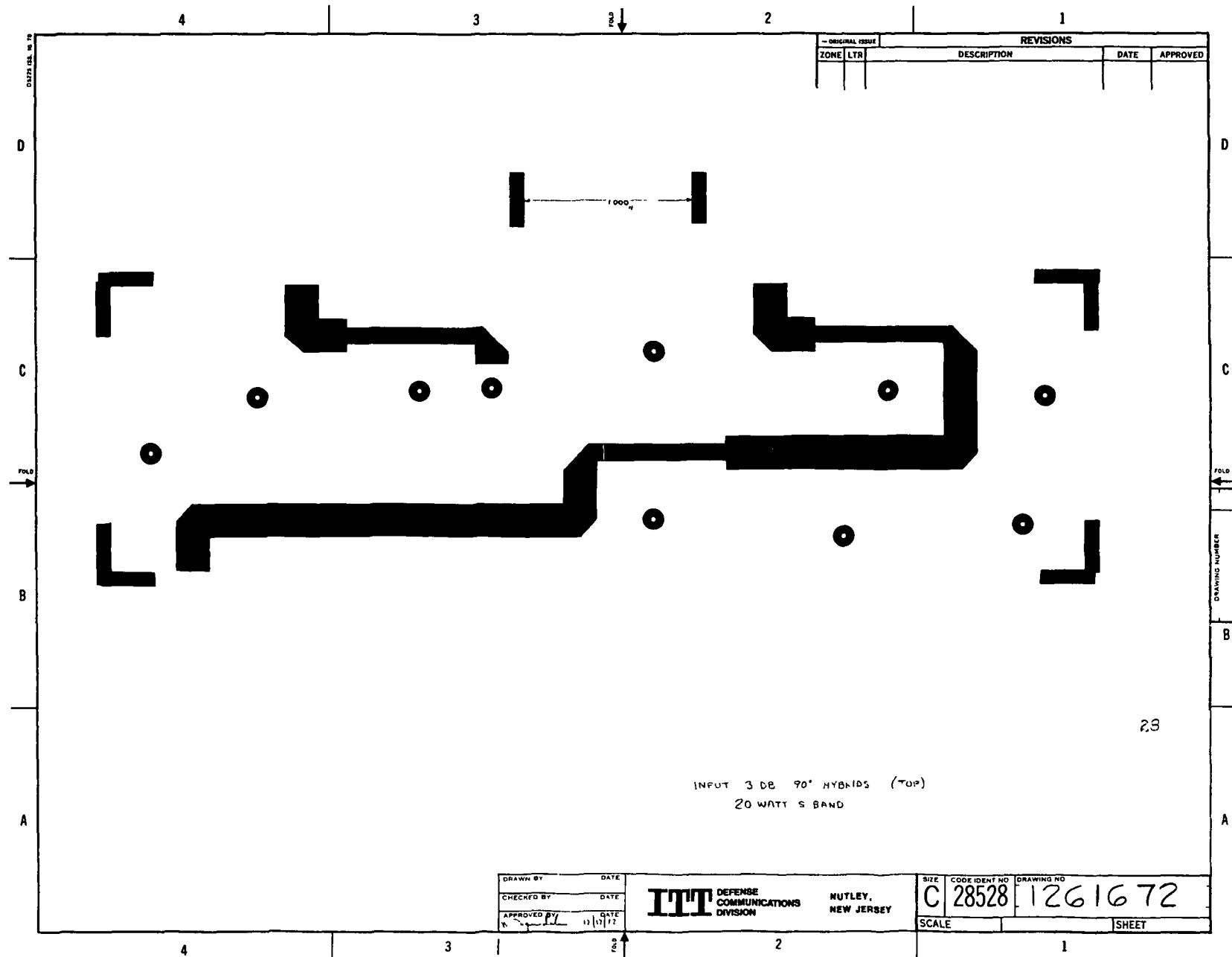
SPECIFICATIONS, ELECTRICAL

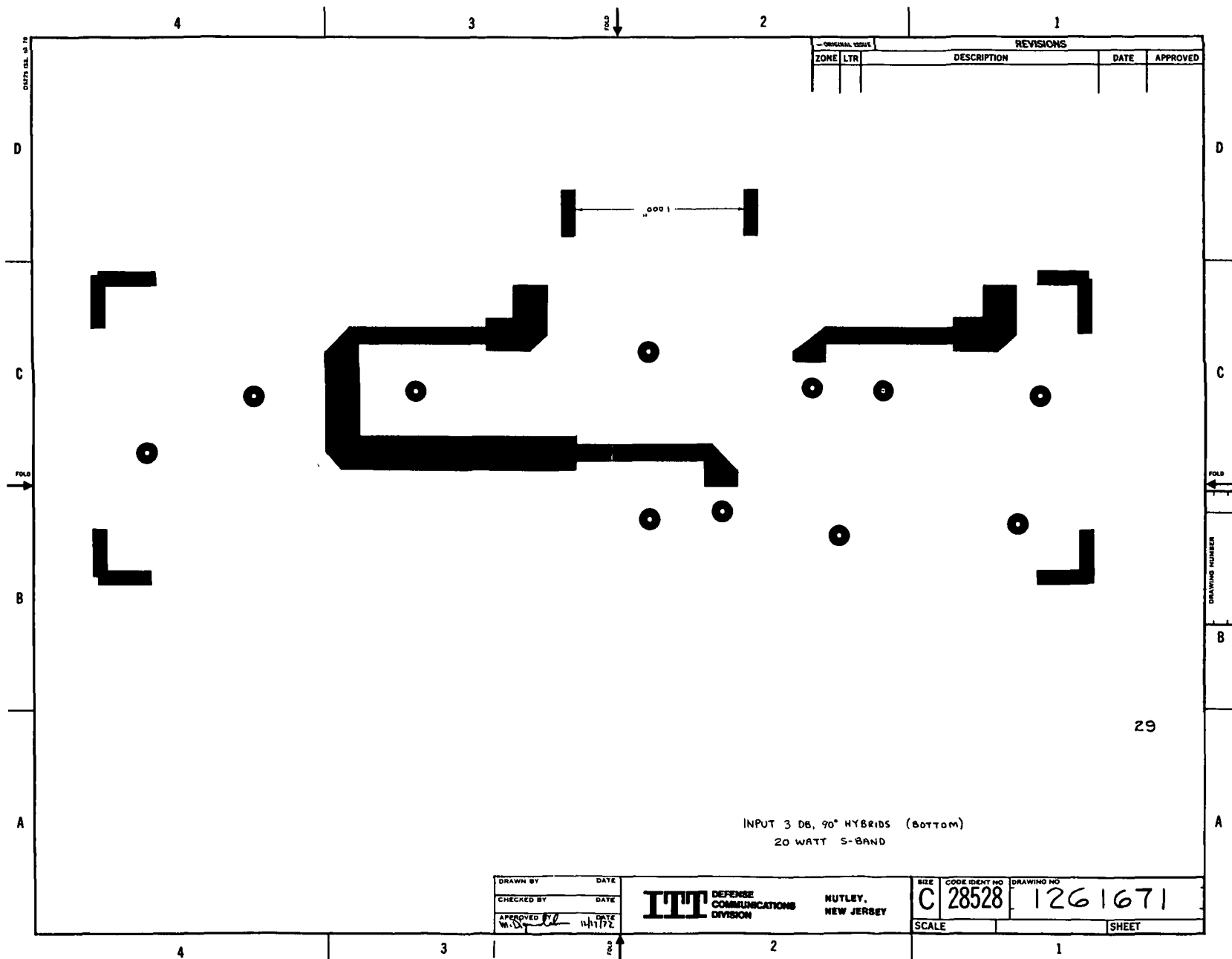
BAND PASS	22 - 23 GHz
MID BAND INSERTION LOSS	0.4 DB MAX
BAND EDGES INSERTION LOSS	0.65 DB MAX
REJECTION AT 21 GHz	45 DB MIN
REJECTION 4 TO 10GHz	40 DB MIN
INPUT/OUTPUT IMPEDANCE	50 OHMS
PASS BAND FLATNESS	0.2 DB
INPUT/OUTPUT VSWR	1.25:1 MAX
PHASE DEVIATION FROM LINEARITY	16 DEGREES OVER 100 MHz
OPERATING TEMPERATURE	-20°C TO +70°C

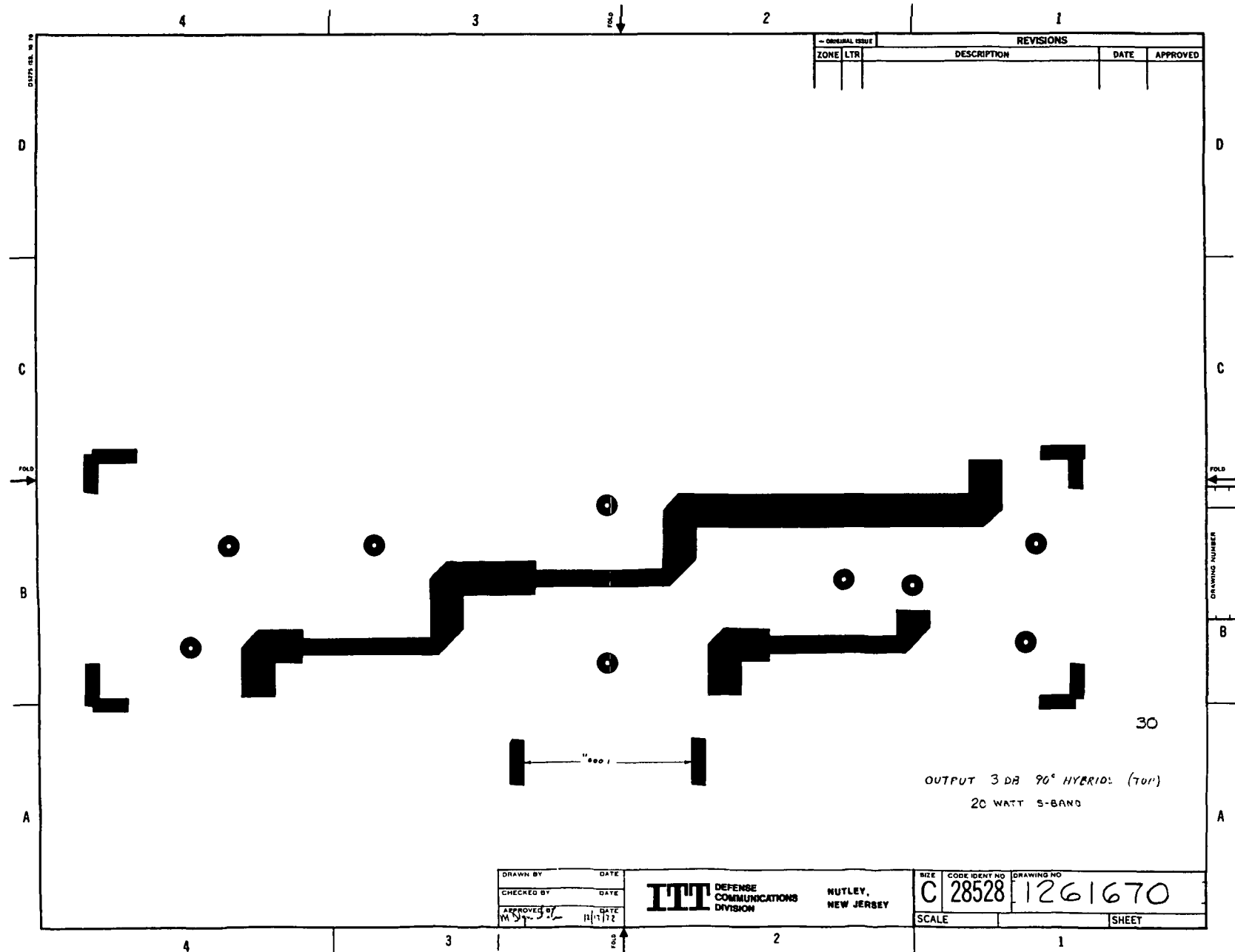
BAND PASS FILTER OUTLINE
(22 - 23 GHz)

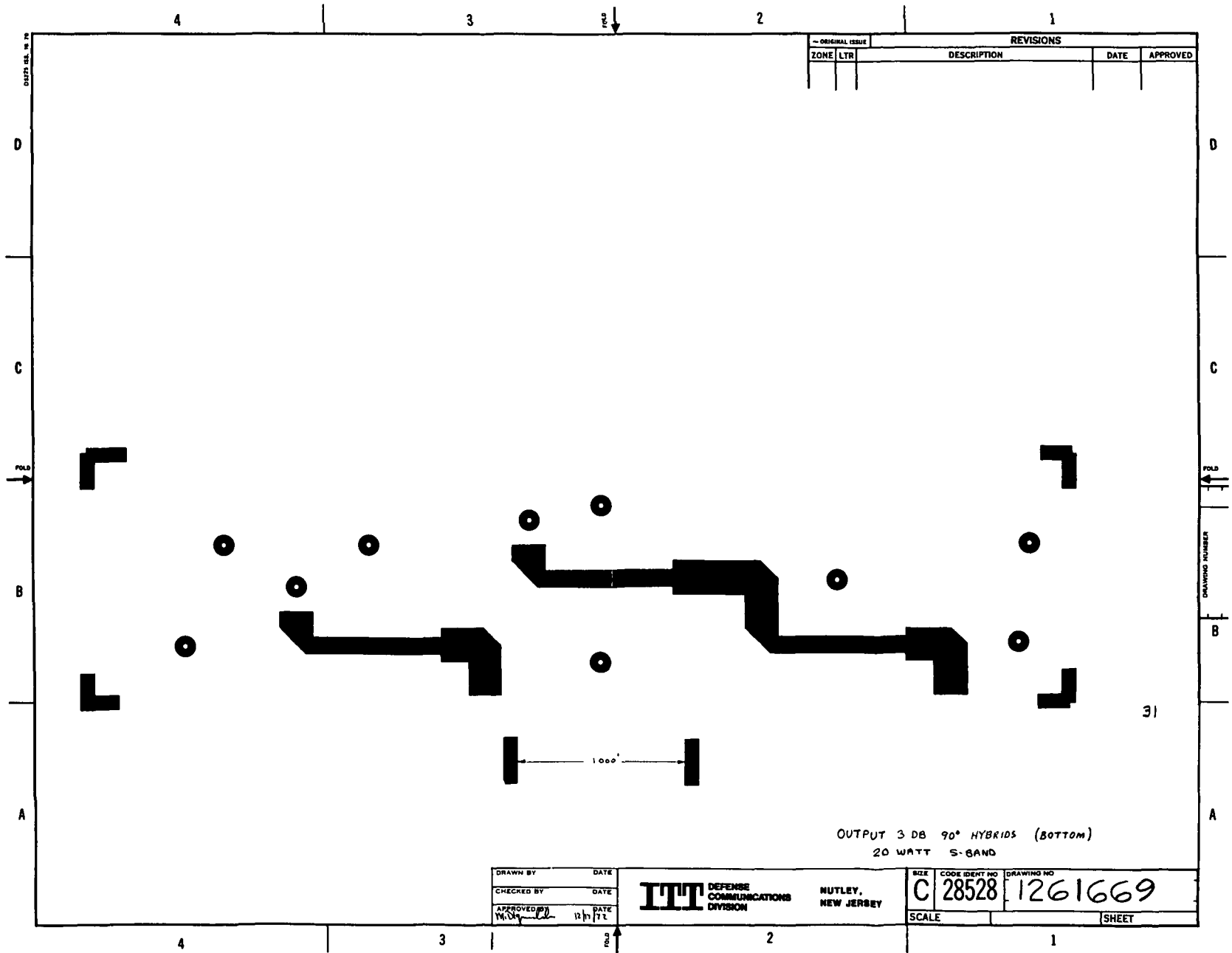
26

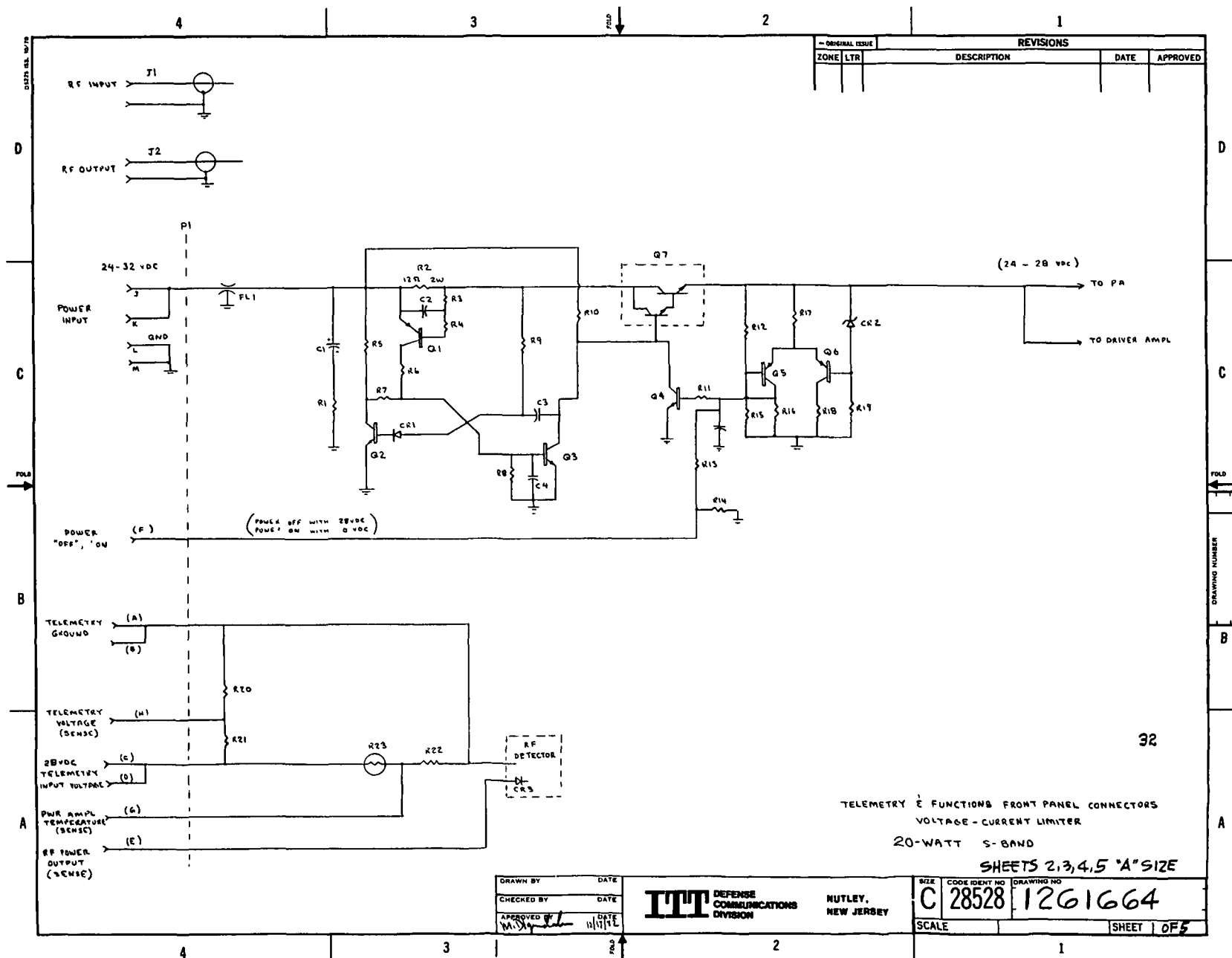
DRAWN BY	DATE	DEFENSE COMMUNICATIONS DIVISION NUTLEY, NEW JERSEY	SIZE	CODE IDENT NO	DRAWING NO
CHECKED BY	DATE		C	28528	1261674
APPROVED BY	DATE		SCALE		SHEET
W. J. [Signature]	12/17/72				26

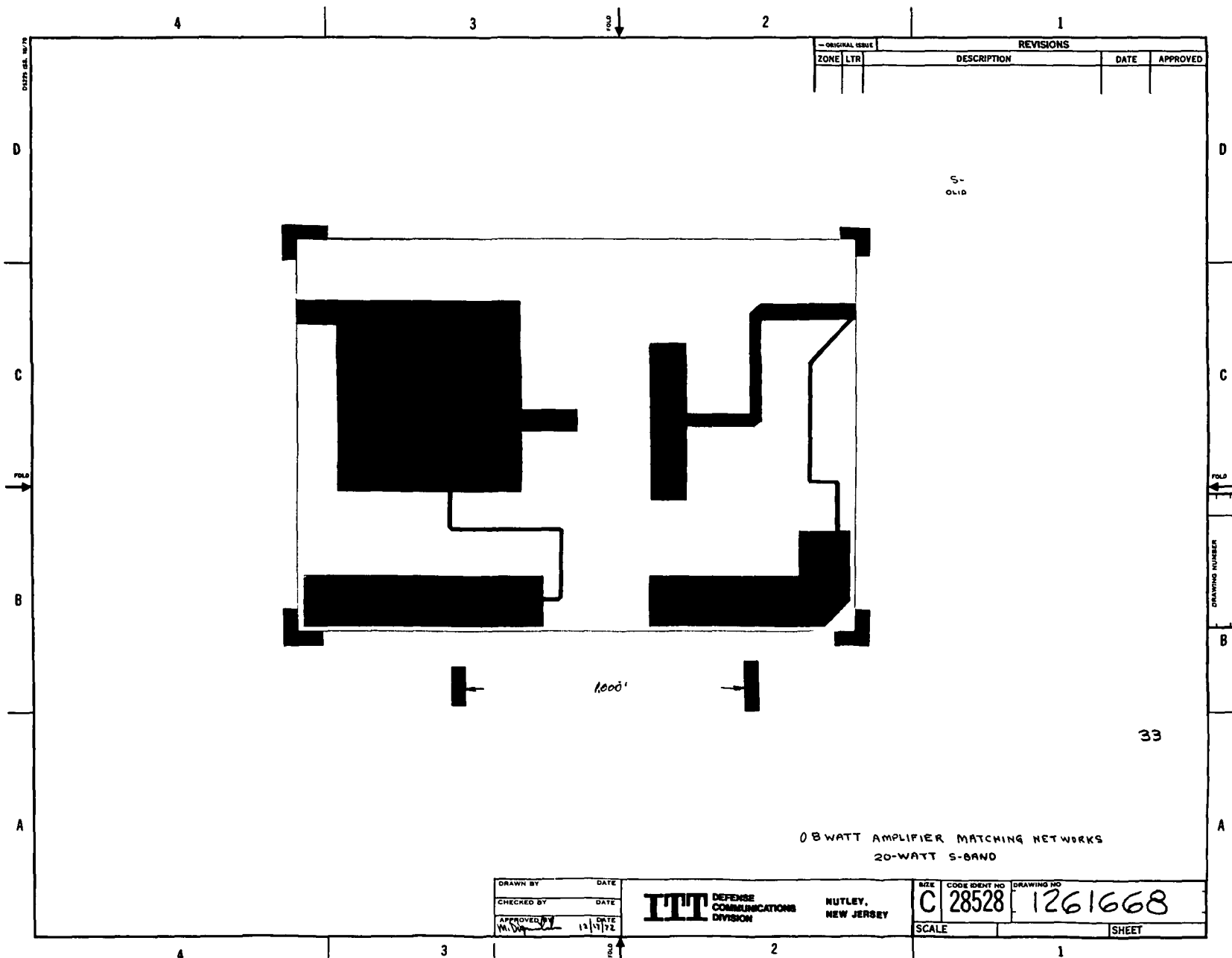


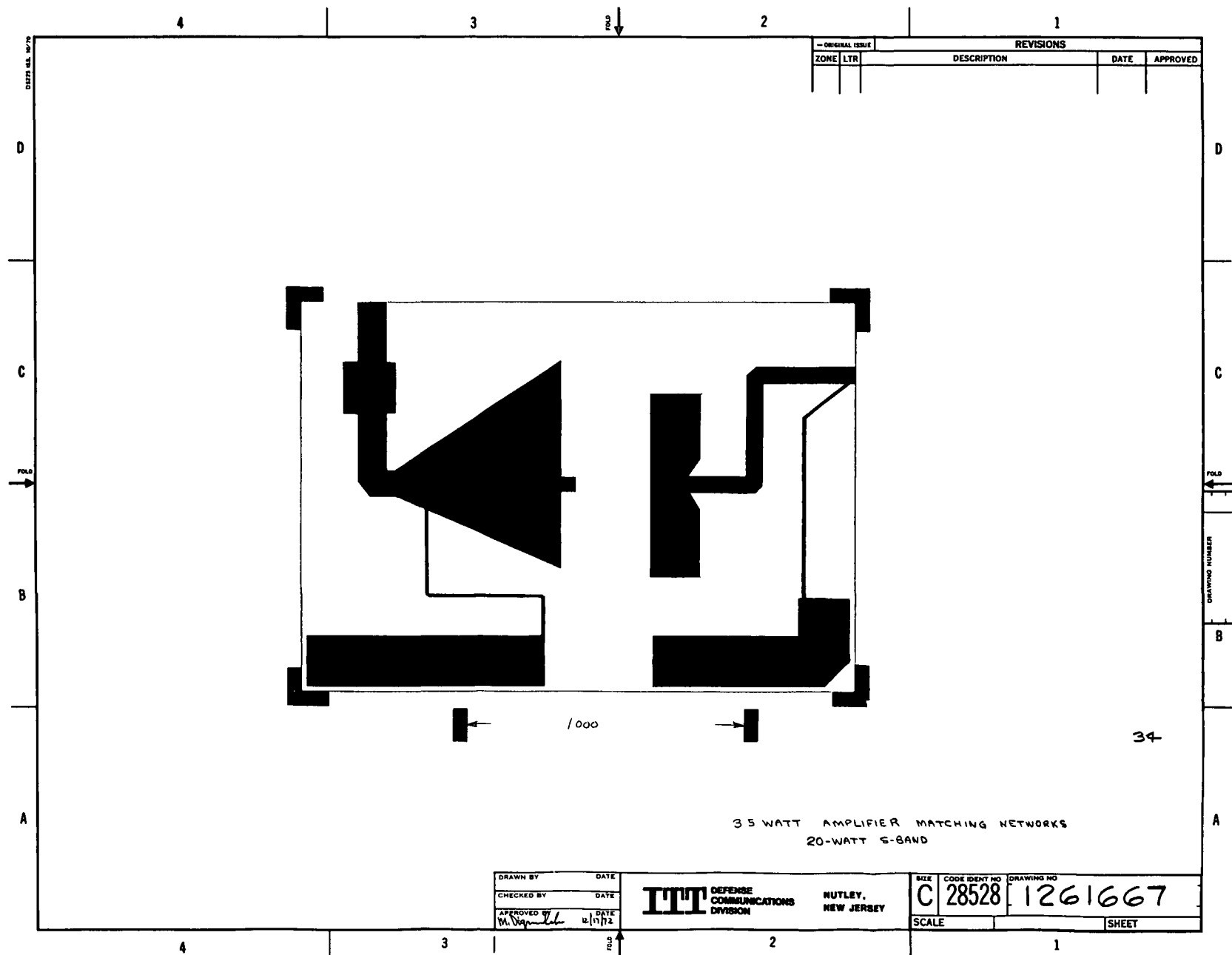


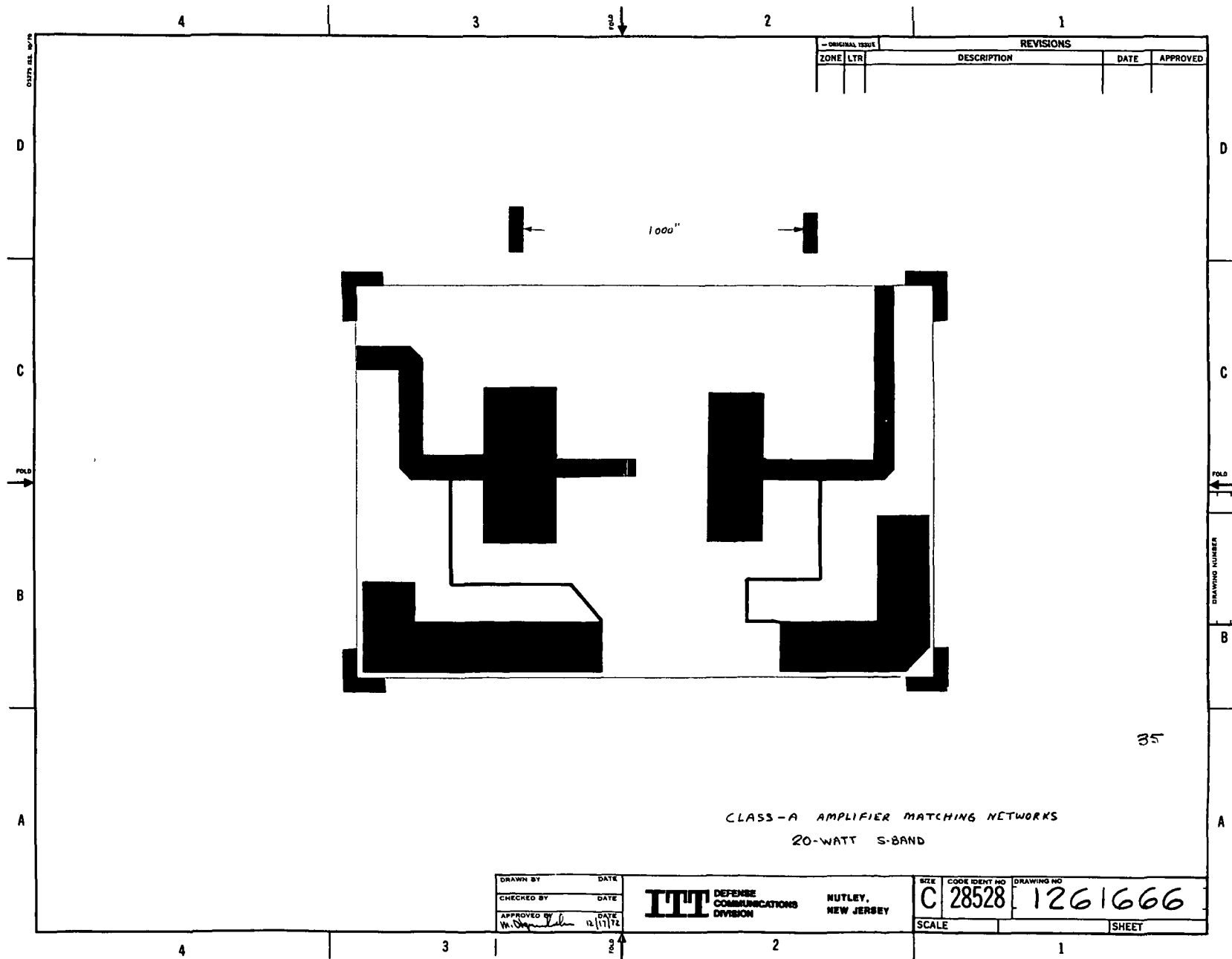


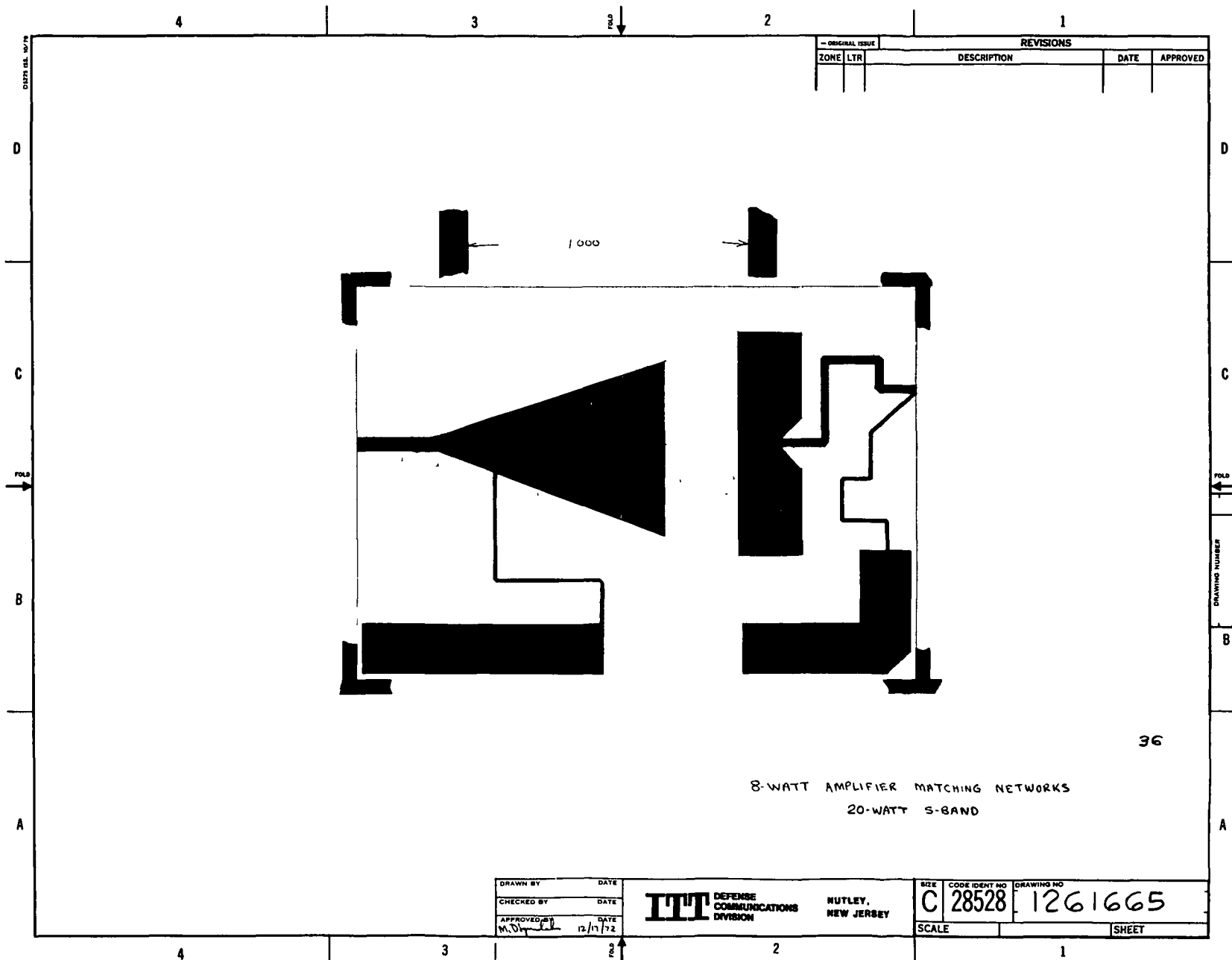


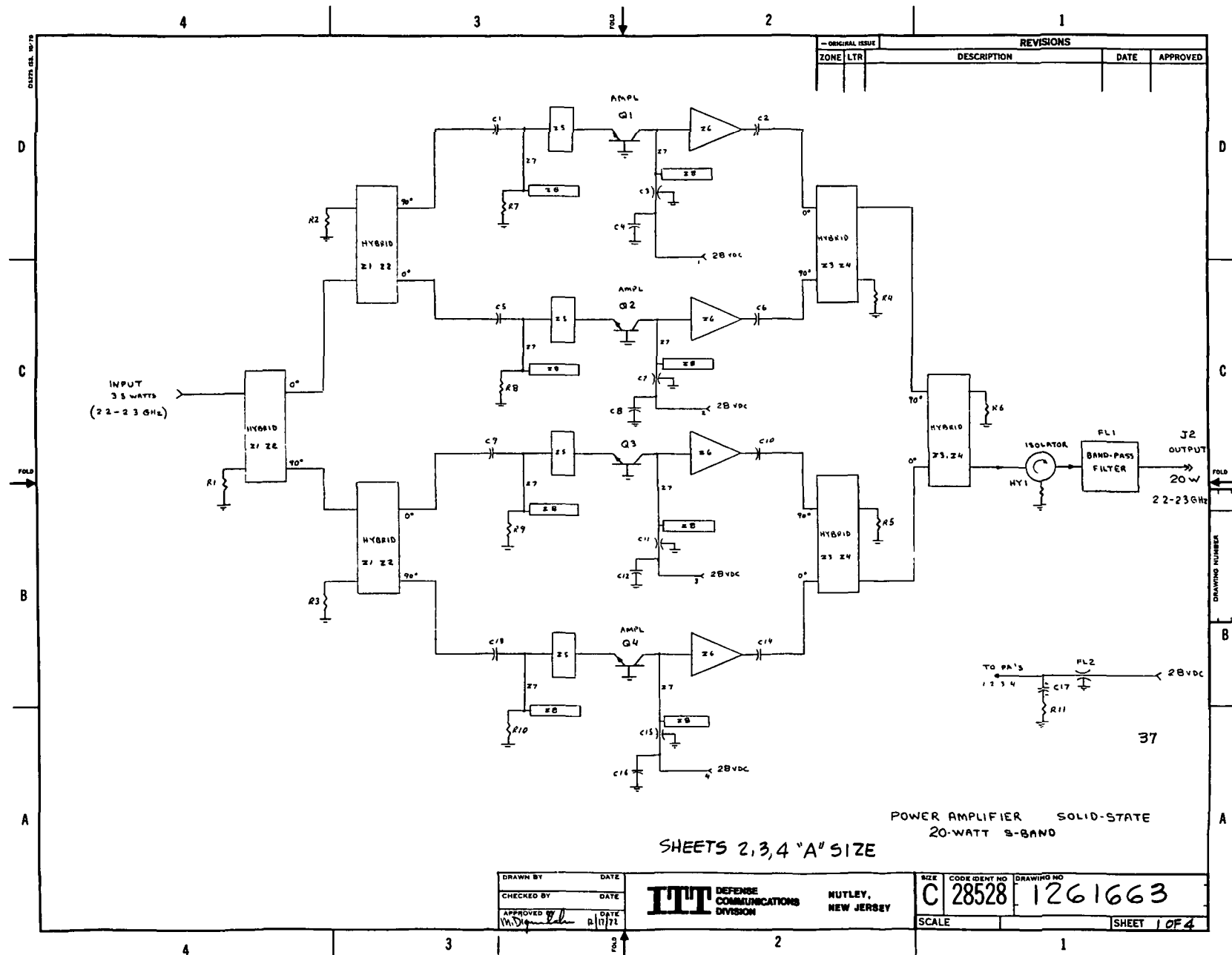


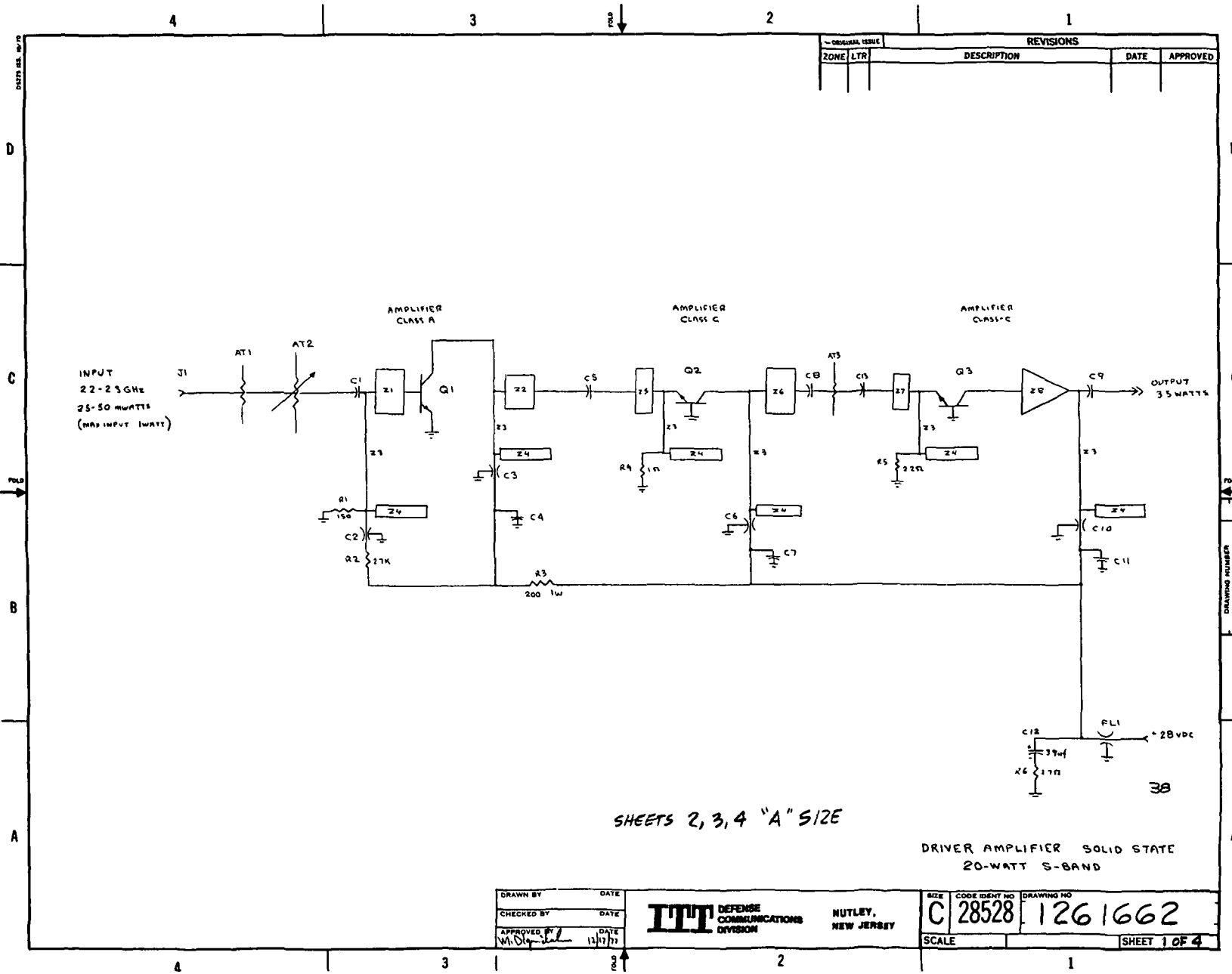






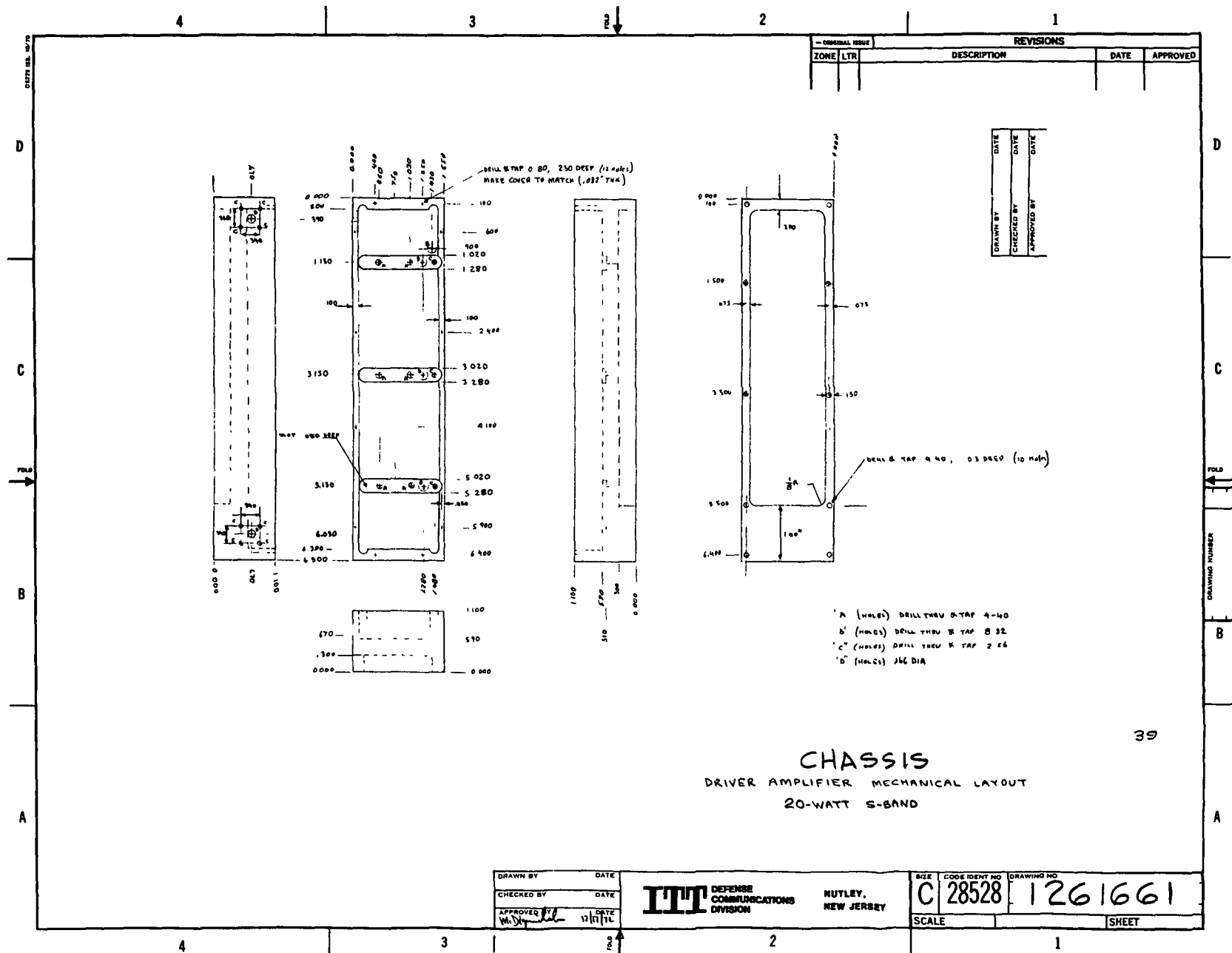


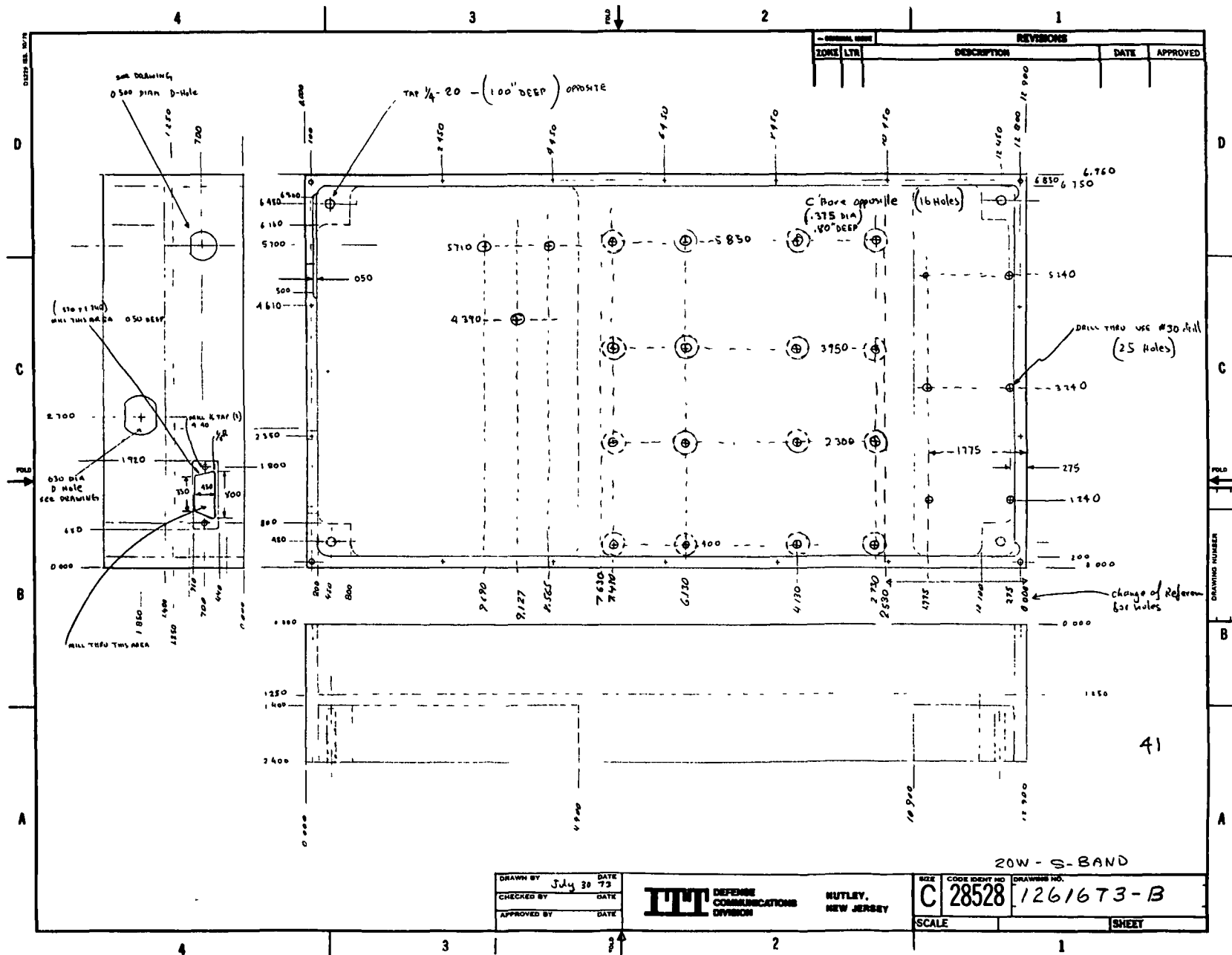




ORIGINAL ISSUE		REVISIONS		
ZONE	LTR	DESCRIPTION	DATE	APPROVED

DRAWN BY	DATE	ITT DEFENSE COMMUNICATIONS DIVISION NUTLEY, NEW JERSEY	SIZE	CODE IDENT NO	DRAWING NO
CHECKED BY	DATE		C	28528	1261662
APPROVED BY	DATE		SCALE	SHEET 1 OF 4	






PARTS LIST					ITT DEFENSE COMMUNICATIONS DIVISION NUTLEY, NEW JERSEY		CONTRACT NO. NAS 8-28763		DWG A SIZE	CODE 28528	DRAWING NUMBER PL 1261662	REV LTR SHT 2/4	
SEE COVER SHEET FOR LIST TITLE, REVISION AND AUTHENTICATION											20-WATT S-BAND AMPL (DRIVER AMPLIFIER)	SH 1" C" SIZE	
QUANTITY PER GROUP					U OF M	ITEM OR FIND NO.	CODE IDENT	SIZE	PART OR IDENTIFYING NO. OTHER THAN ITTDCD	SIZE	SPECIFICATION NO. ITTDCD PART NO.	NOMENCLATURE OR DESCRIPTION	ITTDCD SOURCE
G5	G4	G3	G2	G1									
									MODEL 3M-6dB			ATTENUATOR, FIXED, 6 DB, 2WATT	AT1
												WEINSCHEL	
									MODEL 404 AA			ATTENUATOR, VARIABLE, 1-10 dB	AT2
												1 dB STEPS KAY ELECTRIC CO.	
									A B 88 B			ATTENUATOR, FIXED, 3 dB, SULLITON	AT3
									RC07			RESISTOR, FIXED, 150 OHMS, ± 5%	R1
									RC07			RESISTOR, FIXED, 2.7 K OHMS, ± 5%	R2
									RC32			RESISTOR, FIXED, 200 OHMS, ± 5%	R3
									RC20			RESISTOR, FIXED, 1 OHM, ± 5%	R4
									RC20			RESISTOR, FIXED, 2.2 OHMS, ± 5%	R5
									RC20			RESISTOR, FIXED, 27 OHMS, ± 5%	R6
									MSC 80064			TRANSISTOR, NPN, SILICON, MSC	Q1
									MSC 4000			TRANSISTOR, NPN, SILICON, MSC	Q2
									MSC 4003			TRANSISTOR, NPN, SILICON, MSC	Q3

U OF M 1 PIECE 6 PAIR 32 FEET 52 U.S. FLUID OZ. 55 U.S. GAL. * IN PART NO. COL. DENOTES VENDOR ITEM. SEE SOURCE OR SPECIFICATION CONTROL DWG.

[illegible]

PARTS LIST					 DEFENSE COMMUNICATIONS DIVISION NUTLEY, NEW JERSEY		CONTRACT NO. NAS 8 - 28763		DWG A SIZE	CODE 28528	DRAWING NUMBER PL 1261663	REV LTR	
SEE COVER SHEET FOR LIST TITLE, REVISION AND AUTHENTICATION					20-WATT S-BAND (POWER AMPLIFIER-HYBRIDS)					SH 1 "C" SIZE		SHT 2/4	
QUANTITY PER GROUP					U OF M	ITEM OR FIND NO.	CODE IDENT	SIZE	PART OR IDENTIFYING NO OTHER THAN ITTDCD	SIZE	SPECIFICATION NO. ITTDCD PART NO.	NOMENCLATURE OR DESCRIPTION	ITTDCD SOURCE
G5	G4	G3	G2	G1									
									ATC100A510KC			CAPACITOR, FIXED, CHP, 51pf, ±5%, ATC	C1
												CAPACITOR, SAME AS C1	C2
									STYLE CKR75			CAPACITOR, FIXED, CERAMIC, 100 pf	C3
									CKR06CW103M			CAPACITOR, FIXED, CERAMIC, .01pf ±20%	C4
												CAPACITOR, SAME AS C1	C5
												CAPACITOR, SAME AS C1	C6
												CAPACITOR, SAME AS C3	C7
												CAPACITOR, SAME AS C4	C8
												CAPACITOR, SAME AS C1	C9
												CAPACITOR, SAME AS C1	C10
												CAPACITOR, SAME AS C3	C11
												CAPACITOR, SAME AS C4	C12
												CAPACITOR, SAME AS C1	C13
												CAPACITOR, SAME AS C1	C14
												CAPACITOR, SAME AS C3	C15
												CAPACITOR, SAME AS C4	C16
	45								K3R9J50K			CAPACITOR, TANTALUM, 3.9 pf ±10%	C17
												KEMET	

U OF M CODE | 1 PIECE | 6 PAIR | 32 FEET | 52 U.S. FLUID OZ. | 55 U.S. GAL. | 54 U.S. LIQUID QT. | 68 LB AVDP | * IN PART NO. COL. DENOTES VENDOR ITEM. SEE SOURCE OR SPECIFICATION CONTROL DWG.

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED
DATE 08-15-2011 BY 60322 UCBAW

U OF M CODE	1 PIECE 5 SET	6 PAIR 20 REF DOC	32 FEET	52 U.S. FLUID OZ. 54 U.S. LIQUID QT.	55 U.S. GAL. 68 LB AVDP	* IN PART NO. COL. DENOTES VENDOR ITEM; SEE SOURCE OR SPECIFICATION CONTROL DWG.
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